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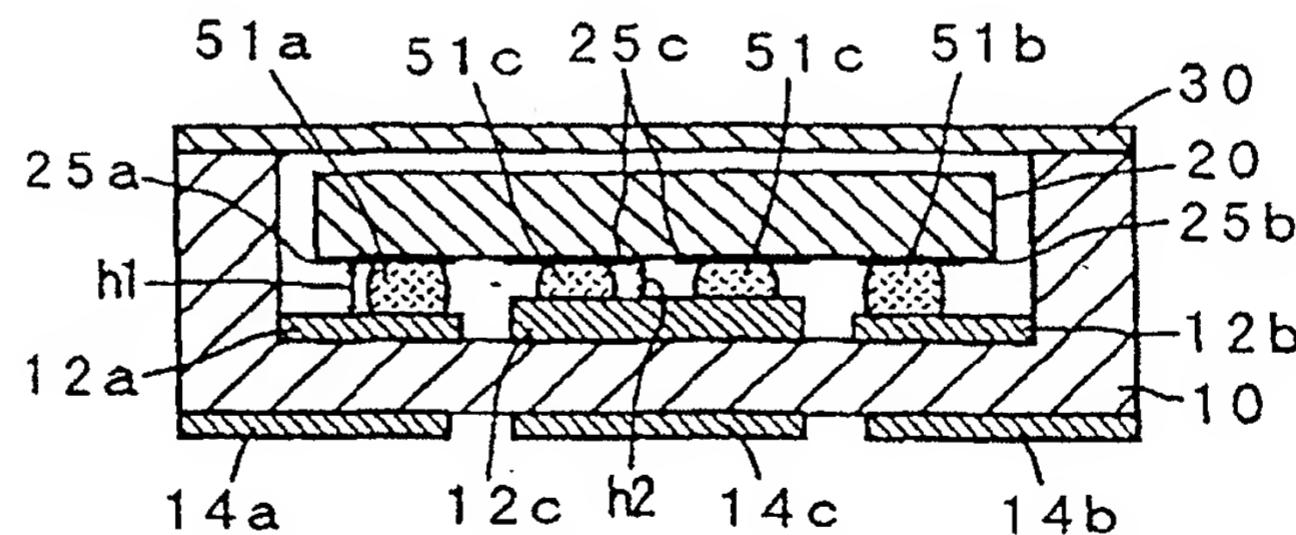
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(54) Electronic component

(57) An electronic component includes an electronic component element (20) electrically and mechanically joined to a base member (10), a plurality of electrode pads (25a-c) of the electronic component element (20) and corresponding electrode lands (12a-c) of the base member (10) being respectively joined together via

bumps (51a-c) such that the electronic component element (20) is arranged opposite to the base member (10). The bump(s) (51a,b) located along the peripheral portion of the electronic component element (20) have a greater height (h1) than that (h2) of the bump(s) (51c) located at the central portion (C) of the electronic component element (20).

FIG. 1



EP 1 041 715 A2

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Description**BACKGROUND OF THE INVENTION****1. Field of the Invention**

[0001] The present invention relates to an electronic component in which an electronic component element, such as a surface acoustic wave element or a semiconductor element, is electrically and mechanically joined to a base member via bump bonding.

2. Description of the Related Art

[0002] As electronic components in which electronic component elements are mounted on base members by bump bonding, a surface acoustic wave device has been known, for example, which has the structure shown in Fig. 8. In this surface acoustic wave device, a surface acoustic wave element 20 is electrically and mechanically bonded to a base member 10. While a propagation surface of the surface acoustic wave element 20 is opposed to the base member 10, input/output electrode pads 25a and 25b and an electrode pad for grounding 25c of the surface acoustic wave element 20 are respectively joined to corresponding input/output electrode pads 12a and 12b and an grounding electrode pad 12c of the base member 10 via bumps 51a to 51c which are formed of a material such as Au. Each of the bumps 51a to 51c is formed to have the same height. Then, a cap member 30 is joined to the base member 10 so as to cover the surface acoustic wave element 20, so that the surface acoustic wave element 20 is hermetically sealed within a package defined by the base member 10 and the cap member 30.

[0003] However, in a conventional surface acoustic wave device, when a mechanical stress such as an impact caused by dropping the device or a thermal stress such as variation in ambient temperature is applied thereto, mechanical damages such as fracture and exfoliation are produced in bumps 51a and 51b and electrode pads 25a and 25b which are respectively bump-bonded thereto. Such mechanical damage usually occurs in a peripheral portion of the base member 10 and the surface acoustic wave element 20, and the failure in the bump bonding process causes the electronic device to have faulty characteristics. This is because the damage caused by the mechanical stress or the thermal stress is often concentrated on bumps located along a peripheral portion instead of bumps located at the central portion.

SUMMARY OF THE INVENTION

[0004] In order to overcome the problems described above, preferred embodiments of the present invention provide an electronic component wave device which achieves a high degree of reliability and prevents prob-

lems and faulty connections conventionally occurring during bump bonding by relieving the stress applied to the bumps located along a peripheral portion of a base member and an electronic component element.

[0005] According to a preferred embodiment of the present invention, an electronic component includes a base member having a plurality of electrode lands, an electronic component element having a plurality of electrode pads and being electrically and mechanically joined to the base member, a plurality of bumps located between the electronic component element and the base member, the plurality of electrode pads of the electronic component element and the corresponding electrode lands of the base member being respectively joined together via the bumps such that the electronic component is opposite to the base member, wherein the bumps located at the peripheral portion of the electronic component element have a greater height than that of the bumps located in the central portion of the electronic component element.

[0006] As a result of this unique structure, the stress applied to the bumps located at the peripheral portion is relieved because the bumps located at the peripheral portion, in which the stress is prone to concentrate, have a greater height than that of the bumps located in the central portion. Consequently, bonding problems such as damage to electrodes or bump bonding portions and exfoliation are reliably prevented, resulting in improved reliability of the electronic component.

[0007] In order to obtain bumps having a greater height at the peripheral portion than that of the bumps located at the central portion, various methods such as changing the film thickness of electrode lands formed on the base member; forming the base member to have a stepped shape; forming the base member to have a convexly-curved shape; or changing the film thickness of electrode pads of the surface acoustic wave element, may be used. Also, any combination of these methods may also be used.

[0008] In the above-mentioned configuration, it is preferable that the differences between heights of the bumps are about 1 µm to about 10 µm.

[0009] In addition, preferred embodiments of the present invention provide an electronic component in which a surface acoustic wave element or a semiconductive element, or other suitable electronic component element, is electrically and mechanically joined to a base member via bump bonding.

[0010] According to preferred embodiments of the present invention, the bumps located at the peripheral portion, in which the stress is prone to concentrate, have a greater height than that of the bumps located at the central portion so as to relieve the stress applied to the bumps located at the peripheral portion, so that bonding problems such as damage to electrodes located at bumps or bump bonding portions, and exfoliation, are reliably prevented.

[0011] For the purpose of illustrating the invention,

there is shown in the drawings several forms which are presently preferred, it being understood, however, that the invention is not limited to the precise arrangements and instrumentalities shown.

BRIEF DESCRIPTION OF THE DRAWINGS

[0012] Fig. 1 is a sectional view of a surface acoustic wave device according to a first preferred embodiment of the present invention.

[0013] Fig. 2 is a plan view of a surface acoustic wave element according to the first preferred embodiment of the present invention.

[0014] Fig. 3 is a sectional view of a surface acoustic wave device according to a second preferred embodiment of the present invention.

[0015] Fig. 4 is a sectional view of a surface acoustic wave device according to a third preferred embodiment of the present invention.

[0016] Fig. 5 is a sectional view of a surface acoustic wave device according to a fourth preferred embodiment of the present invention.

[0017] Fig. 6 is a graph showing the relationship between the height of bumps and stress caused by thermal stress.

[0018] Fig. 7 is a graph showing the relationship between a difference in bump heights and the failure rate due to stress.

[0019] Fig. 8 is a sectional view of a conventional surface acoustic wave device.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0020] Hereinafter, preferred embodiments of the present invention are explained in detail with reference to the drawings.

[0021] An electronic component according to one preferred embodiment of the present invention will be described below in the context of a surface acoustic wave device. However, other electronic component elements may also be included in other types of electronic components of the present invention.

[0022] A structure of a surface acoustic wave device according to a first preferred embodiment of the present invention is shown in Figs. 1 and 2. Fig. 1 is a sectional view of the surface acoustic wave device while Fig. 2 is a plan view of a surface acoustic wave element.

[0023] In the surface acoustic wave device according to the present preferred embodiment, a plurality of electrode pads 25a to 25c, of a surface acoustic wave element 20 and a plurality of electrode lands 12a to 12c on the top surface within a recessed area of a base member 10 are respectively bump-bonded together via bumps 51a to 51c and a cap member 30 is joined to the base member 10 so as to cover the surface acoustic wave element 20, so that the surface acoustic wave element 20 is hermetically sealed within a package (within a

space) defined by the base member 10 and the cap member 30.

[0024] The base member 10 preferably has a recessed shape and is preferably formed by stacking a plurality of layers of ceramic material or other suitable material. On the top surface of the recessed area, which is a mounting surface of the surface acoustic wave element, an electrode pattern including electrode lands for input-output 12a and 12b and an electrode land for grounding 12c is provided. On the bottom surface of the base member 10, terminal electrodes for input-output 14a and 14b and a terminal electrode for grounding 14c are provided. The electrode lands 12a to 12c are preferably made of thick film electrodes of W or Mo, or other suitable material, with Ni or Au preferably being plated thereon. The electrode lands 12a, 12b, and 12c are respectively connected to terminal electrodes 14a, 14b, and 14c via through-hole electrodes or edge electrodes (not shown). This surface acoustic wave device is mounted on a mounting substrate (circuit board) using the bottom surface of the base member 10 as a mounting surface.

[0025] The surface acoustic wave element 20 preferably includes a piezoelectric substrate 21, for example, as shown in Fig. 2. On the top surface of the piezoelectric substrate 21, an electrode pattern including IDT electrodes 22, reflector electrodes 23, and electrode pads 25a to 25c connected to respective IDT electrodes 22, are provided. The electrode pattern is preferably made of Al or an alloy including Al and formed by a known thin film forming method. In addition, in Fig. 2, portions indicated by circles with broken lines indicate locations of bump bonding portions. The piezoelectric substrate 21 is preferably made of a piezoelectric material such as lithium tantalate, lithium niobate, and crystal or other suitable material.

[0026] The surface acoustic wave element 20 is electrically and mechanically joined to a base member 10 in a face-down configuration, that is, the propagation surface of the surface acoustic wave device on which the IDT electrodes 22, and other elements, are provided is opposed to the element-mounting surface of the base member 10. The electrode pads 25a, 25b, and 25c are respectively joined to corresponding electrode pads 12 of the base member 10 via respective bumps 51a, 51b, and 51c. This bonding is performed by applying heat or simultaneously applying heat and ultrasonic waves to fuse bumps 51a to 51c. The bumps 51a to 51c are preferably made of Au or a metal including Au as the principal ingredient. The bumps 51a to 51c are preferably provided on the electrode pads 25a to 25c of the surface acoustic wave element 20 in advance by a ball bonding method. In addition, solder may be used to form the bump, which may be formed on the electrode land of the base member by a method such as printing.

[0027] An appropriate metallic material such as an alloy including Fe-Ni is preferably used to form the cap member 30, which may be plated thereon as desired.

The cap member 30 is joined to the base member 10 so as to cover the surface acoustic wave element 20, via a soldering material such as solder having a high melting point, an alloy of Au-Sn, or glass having a low melting point or other suitable material.

[0028] In the surface acoustic wave device according to the present preferred embodiment, the film of the electrode land for grounding 12c located at an approximately central portion of the base member 10 has a greater thickness than that of the electrode lands for input-output 12a and 12b located along the peripheral portion. That is, the top surface of the electrode land for grounding 12c is higher than the electrode lands for input-output 12a and 12b. (Here, and below, the relative expressions «higher» and «lower» refer to the device when in the orientation shown in the figures.) Thereby, the bumps 51a and 51b located at the peripheral portion of the surface acoustic wave element 20 have a greater height $\langle h1 \rangle$ than a height $\langle h2 \rangle$ of the bumps 51c located at the central portion of the surface acoustic wave element 20. In order to increase the film thickness of the electrode land for grounding 12c, the electrode film preferably made of W or Mo, or Ni or Au plating has a thickness that is greater than that of the electrode lands for input-output 12a and 12b.

[0029] It is to be noted that the bumps «in the central portion» and the bumps «in the peripheral portion» are referred to in terms of the distances from the approximate center of the surface acoustic wave element 20. More specifically, as shown in Fig. 2, the distance between the circles on the electrode pads 25c which indicate the position of the bumps 51c and the approximate center «C» of the surface acoustic wave element 20 are smaller than the distance between the circles on the electrode pads 25a and 25b which indicate the position of the bumps 51a and 51b and the center «C». That is, the bumps 51a and 51b are located farther than the bumps 51c from the approximate center of the surface acoustic wave element.

[0030] Because of this unique structure and arrangement, when mechanical or thermal stress is applied, the stress applied to the bumps 51a and 51b located along the peripheral portion, in which the stress is apt to concentrate, is relieved, so that mechanical damage such as fracture and exfoliation are prevented from occurring in the bumps and the electrode pads bump-bonded thereto. Therefore, with this preferred embodiment, faulty joining such as breaking of wire in the bump and faulty bump bonding are reliably prevented, resulting in greatly improved reliability of the electronic component.

[0031] A surface acoustic wave device according to a second preferred embodiment will be described with reference to Fig. 3. In this preferred embodiment, a step portion 16 is preferably formed in the base member 10 so that a portion for forming the electrode land for grounding 12c, that is the approximate central portion of the element mounting surface, projects toward the surface acoustic wave element 20. Each of electrode

lands 12a to 12c preferably has substantially the same film thickness. The other structure is similar to that of the first preferred embodiment. That is, in the second preferred embodiment, a portion of the base member 10 has a greater thickness so that the top surface of the electrode land for grounding 12c is higher than the electrode lands for input-output 12a and 12b. As a result, the bumps 51a and 51b located along the peripheral portion of the surface acoustic wave element 20 have a greater height than that of the bumps 51c at the approximate central portion. As a result of this unique structure, the same advantages achieved by the first preferred embodiment are achieved in the second preferred embodiment.

[0032] A surface acoustic wave device according to a third preferred embodiment will be described with reference to Fig. 4. In this preferred embodiment, the base member 10 is convexly curved so that a forming portion of the electrode land for grounding 12c, that is the central portion of the element mounting surface, is close to the surface acoustic wave element 20. Each of electrode lands 12a to 12c preferably has the same film thickness. The other structure of this preferred embodiment is similar to that of the first preferred embodiment. That is, in the third preferred embodiment, the section of the base member 10 preferably has a roughly circularly curved surface so as to increase the height of the central portion of the element mounting surface, so that the top surface of the electrode land for grounding 12c is higher than the electrode lands for input-output 12a and 12b. Thereby, the bumps 51a and 51b located along the peripheral portion of the surface acoustic wave element 20 have a greater height than that of the bumps 51c at the central portion.

[0033] Because of this unique structure and arrangement, the advantages achieved by the preferred embodiments described above are also achieved in the third preferred embodiment. The structure of the third preferred embodiment is simple in comparison to the structure of the first and second preferred embodiments, resulting in easy manufacturing.

[0034] A surface acoustic wave device according to a fourth preferred embodiment will be described with reference to Fig. 5. In this fourth preferred embodiment, the films of the electrode pads for grounding 25c located at the approximate central portion of the surface acoustic wave element have a larger thickness than that of the electrode pads for input-output 25a and 25b located along the peripheral portion of the surface acoustic wave element. The electrode lands 12a to 12c of the base member 10 preferably have the same film thicknesses. The other structure of the fourth preferred embodiment is preferably similar to that of the first preferred embodiment. That is, in the fourth preferred embodiment, the bottom surfaces of the electrode pads for grounding 25c are lower than those of the electrode pads for input-output 25a and 25b. Thereby, the bumps 51a and 51b located along the peripheral portion of the

surface acoustic wave element 20 have a larger height than that of the bumps 51c at the approximate central portion. Because of this unique structure and arrangement, the advantages achieved by the preferred embodiments described above are also achieved in the fourth preferred embodiment.

[0035] In addition, each of the structures described in the above-mentioned preferred embodiments may be combined to form bumps located along the peripheral portion to have a greater height than that of bumps at the central portion.

[0036] Fig. 6 is a graph showing the relationship between the height of the bump and the stress imparted by thermal stress. In the example shown in Fig. 6, the stresses applied to the bumps are relatively shown when the height of the bump was changed, wherein the base member was formed of alumina, the bump had a diameter of about 120 μm , and two distances between the approximate center of the surface acoustic wave element and the bump of about 300 μm and about 600 μm were evaluated. In addition, as for the stress due to thermal stress, there is a stress produced when the temperature of joining such as bump bonding or joining of the base member to the case member is reduced to room temperature.

[0037] As shown in Fig. 6, the stress applied to the bump decreases with increasing height of the bump, and increases with increasing distance between the approximate center of the surface acoustic wave element and the bump. That is, the stress increases with increasing spacing between the bump forming position and the approximate center of the surface acoustic wave element. Therefore, when the height of bumps is the same, the stress applied to the bumps located along the peripheral portion is relatively larger than that of the bumps located at the approximate central portion.

[0038] Accordingly, in preferred embodiments of the present invention, the stress applied to the bumps located along the peripheral portion is greatly reduced such that the bumps located along the peripheral portion have a greater height than the bumps located in the approximate central portion. The reason is that the stress tends to be absorbed into the bump itself with increasing height of the bump.

[0039] That is, in each of preferred embodiments described above, the film thickness of the electrode land, the thickness of the stepped portion, or the degree of the curvature in the base member or the film thickness of the electrode pad in the surface acoustic wave element is established such that each bump has a greater height in accordance with increasing distance from the approximate center of the surface acoustic wave element.

[0040] Next, the difference in height of bumps will be considered. Fig. 7 is a graph showing the relationship between the difference in height between the bumps located along the peripheral portion and the bumps located in the approximate central portion and a failure rate

due to the mechanical and the thermal stress. More particularly, in Fig. 7, the solid line indicates the failure by exfoliation of the electrode pads for input-output 25a and 25b shown in the respective drawings for the preferred embodiments due to the mechanical and the thermal stress while the broken line indicates the failure caused by insufficient bonding between the electrode lands for input-output 12a and 12b and the bumps 51a and 51b.

[0041] As shown in Fig. 7, when differences in height between the bumps located along the peripheral portion and the bumps located at the approximate central portion are substantially equal to or higher than about 1 μm , the failure due to the mechanical and the thermal stress is prevented. However, when differences in height between bumps are not less than about 10 μm , the bonding between bumps and electrode lands is deteriorated during assembling of the surface acoustic wave device, so that the failure caused by bonding problems such as insufficient bonding between the electrode land and the bump occur. Therefore, it is preferable that differences in height between the bumps range from about 1 μm to about 10 μm , while the bumps located along the peripheral portion have a larger height than the bumps located at the approximate central portion.

[0042] In addition, the electrode pattern is not limited to the one shown in Fig. 2. The surface acoustic wave element may have electrode patterns defined by one or no less than three IDTs. That is, the number and locations of electrode pads of the surface acoustic wave element and the number and locations of electrode lands of the base member are not limited to those in the above-mentioned preferred embodiments. Electrode pads and electrode lands for input-output may be located toward the approximate central portion, while electrode pads and electrode lands for grounding may be located along the peripheral portion.

[0043] The shapes of the base member and the cap member are not limited to those in the above-mentioned preferred embodiments. The package may be formed of a plate shaped base member and a recessed shaped cap member.

[0044] The above-mentioned preferred embodiments have been described in the context of the surface acoustic wave device. However, the present invention may be applied to a semiconductor device in which a semiconductor element is bump-bonded to a base member, or to other electronic components.

[0045] While preferred embodiments of the invention have been disclosed, various modes of carrying out the principles disclosed herein are contemplated as being within the scope of the following claims. Therefore, it is understood that the scope of the invention is not to be limited except as otherwise set forth in the claims.

Claims

1. An electronic component comprising:

- a base member (10) having a plurality of electrode lands (12a-c);
 an electronic component element (20) having a plurality of electrode pads (25a-c) and being electrically and mechanically joined to the base member (10);
 a plurality of bumps (51a-c) located between the electronic component element (20) and the base member (10), the plurality of electrode pads (25a-c) of the electronic component element (20) and the corresponding electrode lands (12a-c) of the base member (10) being respectively joined together via the bumps (51a-c) such that the electronic component element (20) is opposite to the base member (10); wherein
 the bump(s) (51 a,b) located at the peripheral portion of the electronic component element (20) have a greater height (h1) than that (h2) of the bump(s) (51c) located in the approximate central portion (C) of the electronic component element (20).

2. An electronic component according to Claim 1, wherein the electrode land (12c) located at the approximate central portion of the base member (10) has a larger thickness than thicknesses of the electrode lands (12a,b) located along the peripheral portion.

3. An electronic component according to Claim 1 or 2, wherein the base member (10) is arranged such that a forming portion (16) of the electrode land (12c) located at the approximate central portion of the base member projects toward the electronic component element (20).

4. An electronic component according to Claim 1, 2 or 3, wherein the base member (10) is curved so that a forming portion of the electrode land (12c) located at the approximate central portion of the base member (10) is close to the electronic component element (20).

5. An electronic component according to any one of Claims 1 to 4, wherein the electrode pad(s) (25c) located at the approximate central portion (C) of the electronic component element (20) have a larger thickness than those of the electrode pads (25a,b) located along the peripheral portion.

6. An electronic component according to any previous Claim, wherein the differences between heights of the bumps (51) are about 1 μm to about 10 μm .

7. An electronic component according to any previous Claim, wherein the electronic component element (20) is a surface acoustic wave element.

8. An electronic component according to any previous Claim, further comprising a cap member (30) joined to the base member (10) so as to cover the electronic component element (20) and such that the electronic component element (20) is hermetically sealed within a package defined by the base member (10) and the cap member (30).

9. An electronic component according to any previous Claim, wherein the electrode lands (12a-c) are made of thick film electrodes of W or Mo and have Ni or Au plated thereon.

10. An electronic component according to any previous Claim, wherein the electrode pads (25a-c) are made of one of Al and an alloy including Al.

11. An electronic component according to any previous Claim, wherein the bumps (51a-c) are made of one of Au, a metal including Au as the main ingredient, and solder.

12. A method of manufacturing an electronic component, comprising the steps of:
 providing a base member (10) having a plurality of electrode lands (12a-c);
 providing an electronic component element (20) having a plurality of electrode pads (25a-c) and being electrically and mechanically joined to the base member (10);
 forming a plurality of bumps (51a-c) on the base member (10) and/or the electronic component member (20);
 arranging the electronic component element (20) opposite to the base member (10); and
 joining the base member (10) and the electronic component element (20) via the plurality of bumps (51a-c) between the electronic component element (20) and the base member (10), the plurality of electrode pads (25a-c) of the electronic component element (20) and the corresponding electrode lands (12a-c) of the base member (10) being respectively joined together via the bumps (51a-c) such that the electronic component element (20) is opposite to the base member (10); wherein
 the step of forming the bumps (51a-c) includes forming the bump(s) (51 a,b) located at the peripheral portion of the electronic component element (20) to have a greater height (h1) than that (h2) of the bump(s) (51 c) located in the approximate central portion (C) of the electronic component element (20).

13. The method according to Claim 12, wherein the electrode land (12c) located at the approximate central portion of the base member (10) has a larger

thickness than thicknesses of the electrode lands (12a,b) located along the peripheral portion.

14. The method according to Claim 12 or 13, wherein the base member (10) is arranged such that a forming portion (16) of the electrode land (12c) located at the approximate central portion of the base member (10) projects toward the electronic component element (20).
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15. The method according to Claim 12, 13 or 14, wherein the base member (10) is curved so that a forming portion of the electrode land (12c) located at the approximate central portion of the base member (10) is close to the electronic component element (20).
15
16. The method according to any one of Claims 12 to 15, wherein the electrode pad(s) (25c) located at the approximate central portion (C) of the electronic component element (20) have a larger thickness than those of the electrode pads (25a,b) located along the peripheral portion.
20
17. The method according to any one of Claims 12 to 16, wherein the differences between heights of the bumps (51a-c) are about 1 µm to about 10 µm.
25
18. The method according to any one of Claims 12 to 17, wherein the electronic component element (20) is a surface acoustic wave element.
30
19. The method according to any one of Claims 12 to 18, further comprising applying at least one of heat and ultrasonic waves during the step of joining the base member (10) and the electronic component member (20) so as to fuse the bumps (51a-c).
35
20. The method according to any one of Claims 12 to 19, further comprising forming the bumps (51a-c) on the electrode pads (25a-c) of the electronic component element (20) via a ball bonding process.
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FIG. 1

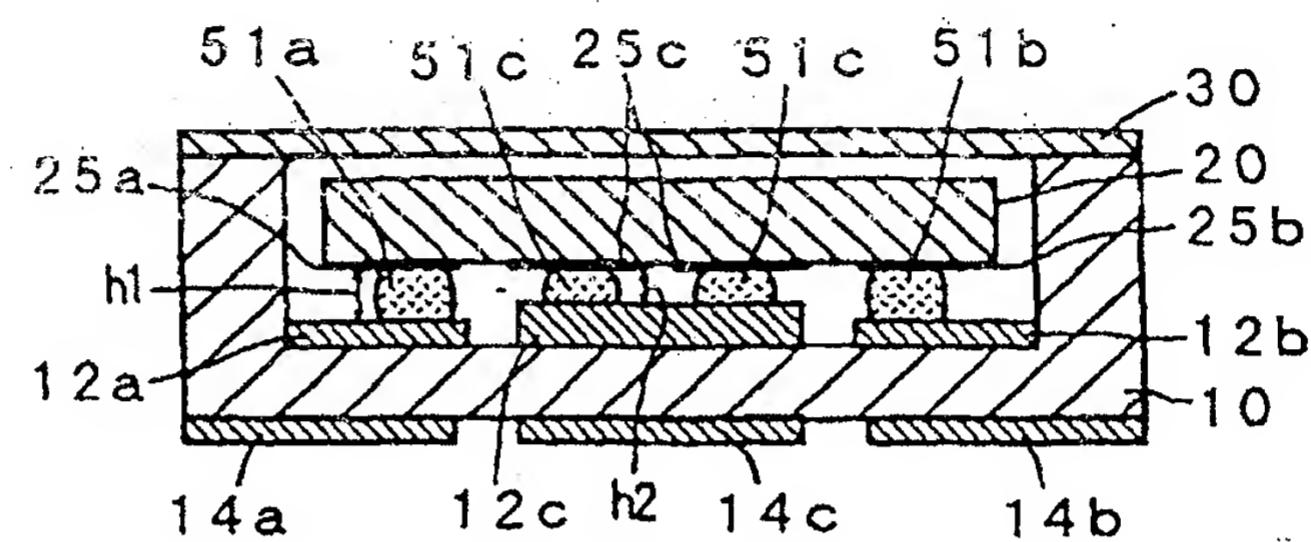


FIG. 2

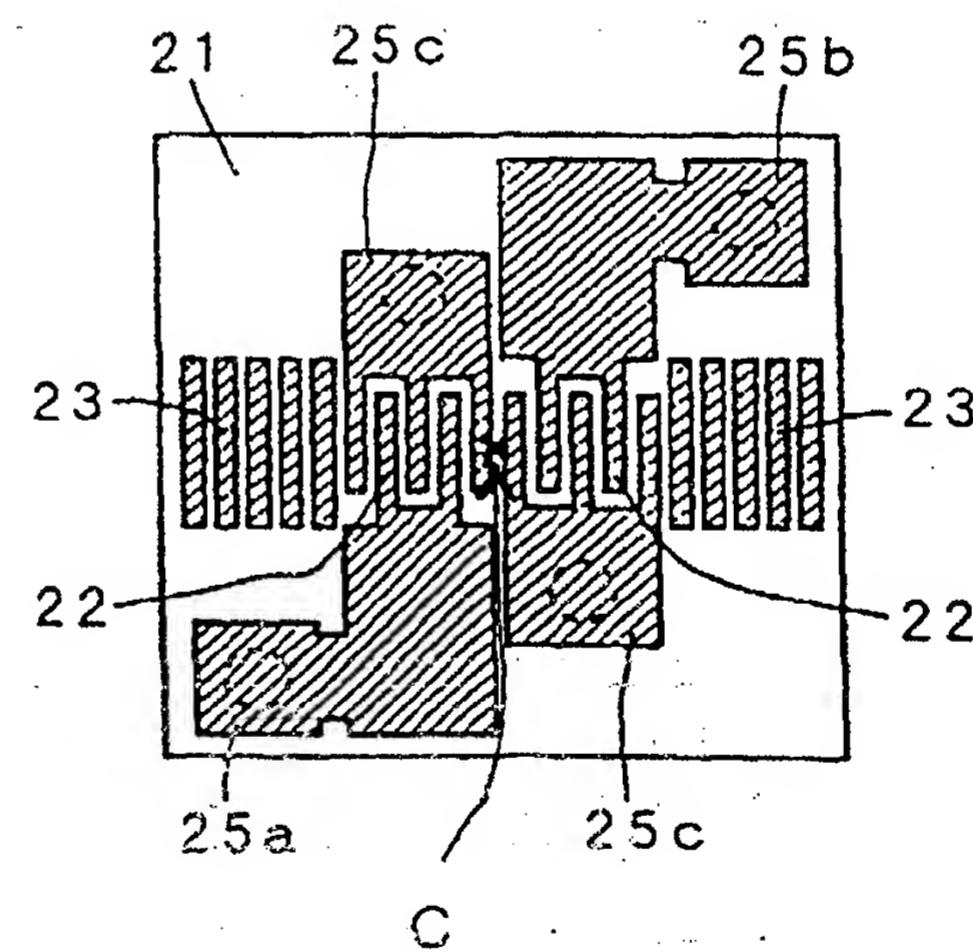


FIG. 3

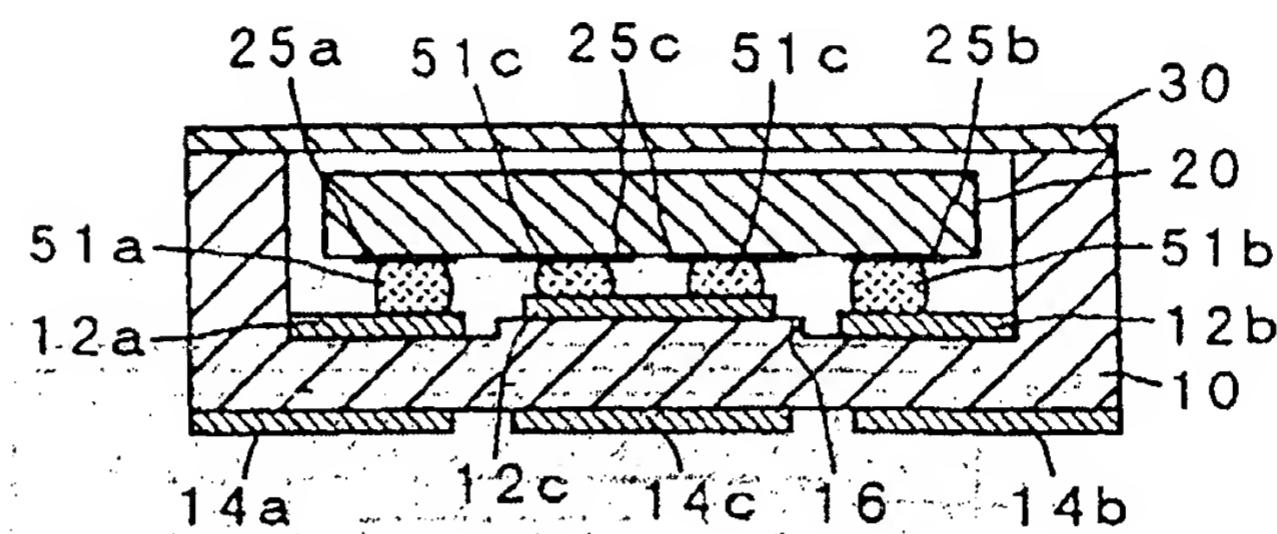


FIG. 4

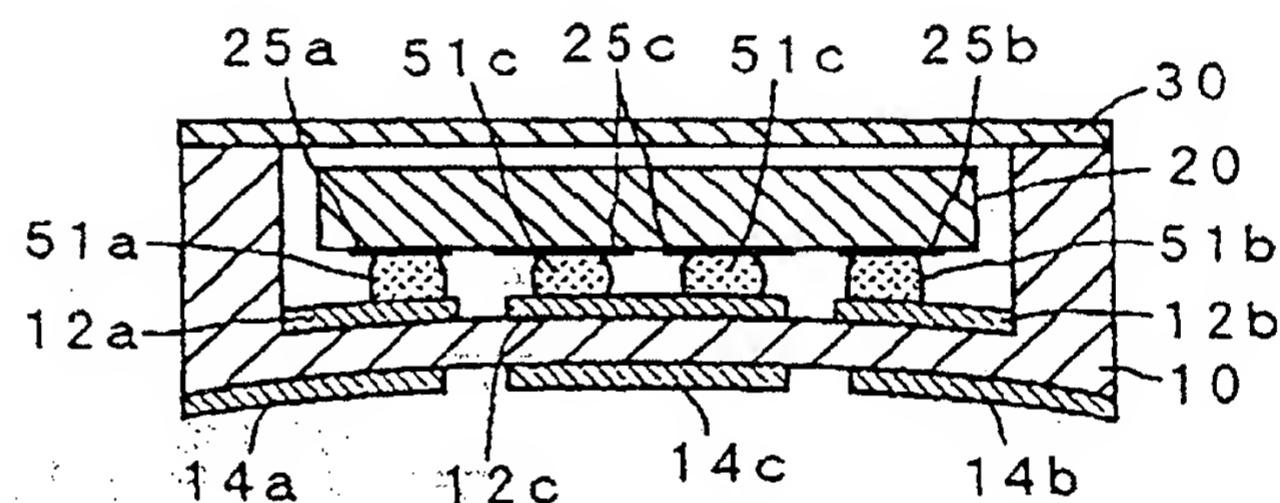


FIG. 5

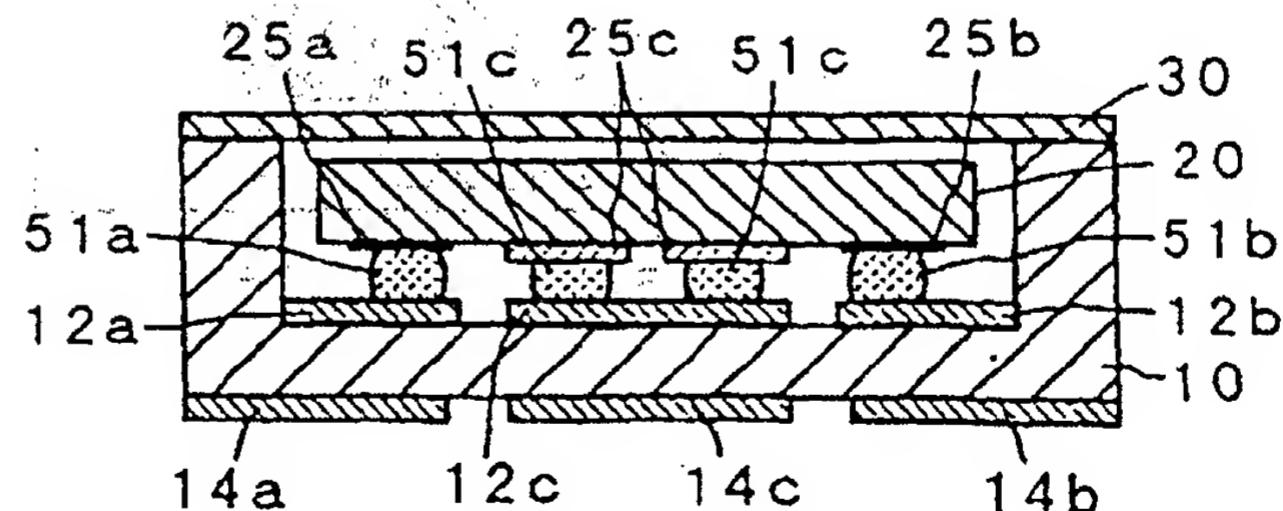


FIG. 6

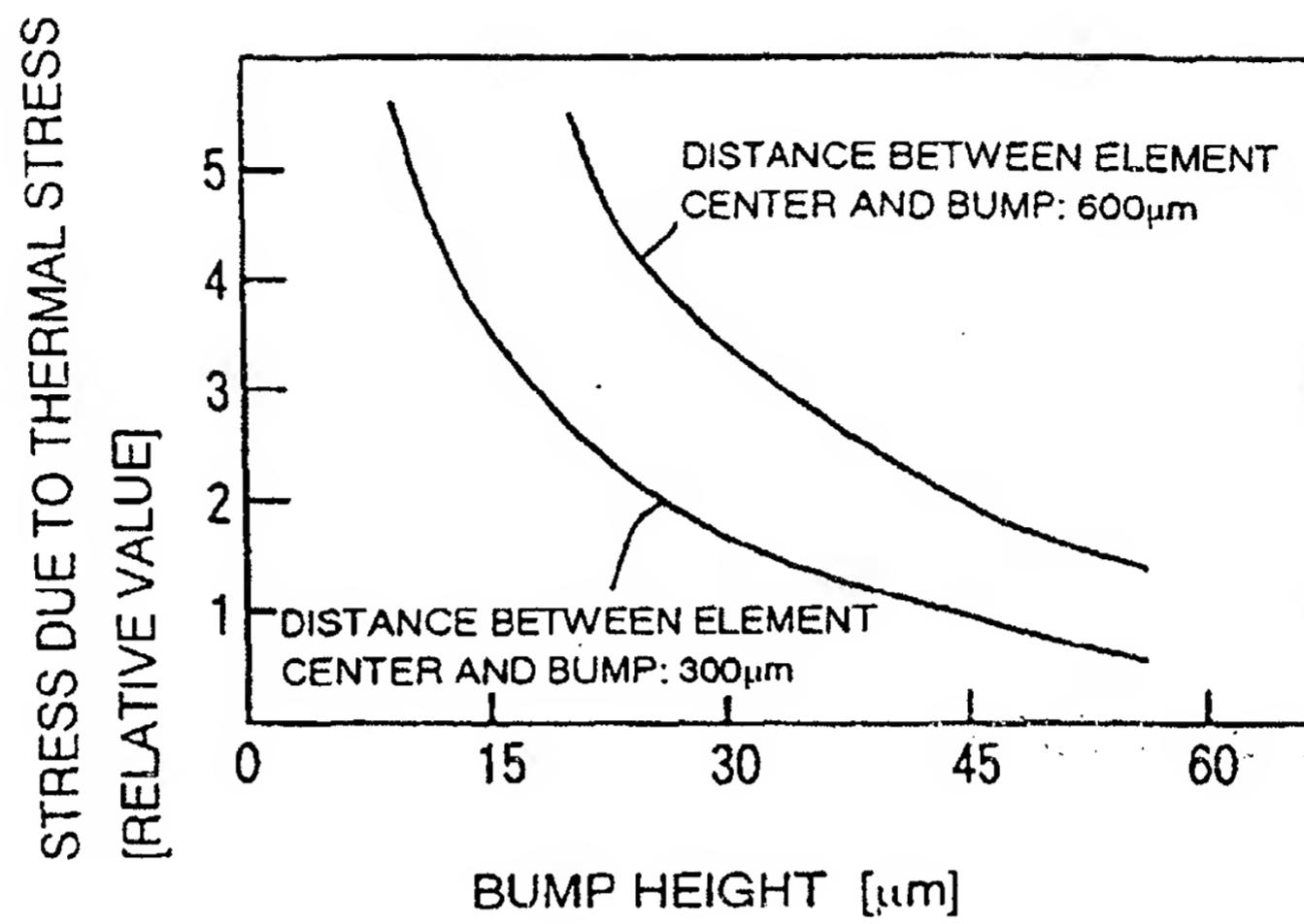


FIG. 7

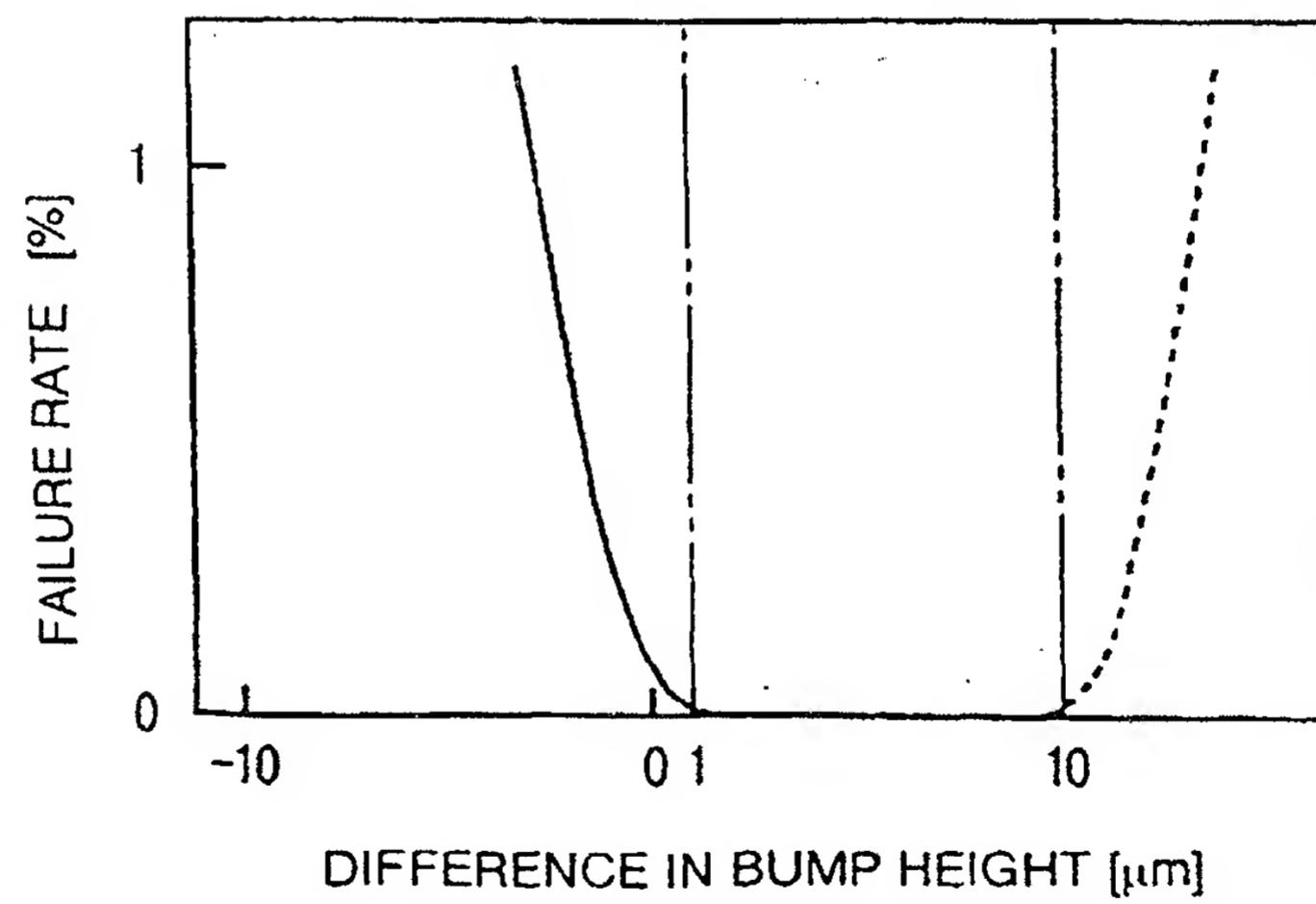


FIG. 8 PRIOR ART

